

## Dynamic Engineers Inc.

Website: <a href="www.DynamicEngineers.com">www.DynamicEngineers.com</a></a>
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#### VCXO3225BM-LJ\_CML-112

Low Jitter VCXO\_Voltage Controlled Crystal Oscillator

### **Features and Benefits**

Frequency range: 15-2100MHz

Output waveform: CML Supply voltage: 2.5V Current: 80mA Max.

Frequency stability vs. temperature: ±20PPM Operating temperature: -10°C to +60°C

Size: 3.2x2.5x1mm

Package type: Surface Mount



#### **Typical Applications**

Defense Systems
Mobile Radar Station
Gigabit Ethernet, SONET/SDH
Server & Storage, Data Center
SD/HD Video, FPGA Clock Generation

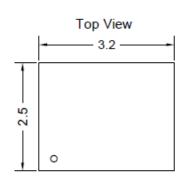
#### **Description**

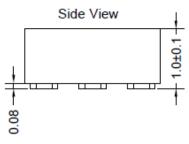
VCXO3225BM-LJ\_CML-112 is the high frequency and low jitter differential VCXO. It can be widely used in digital circuits.

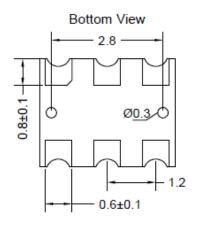
## **Mechanical Drawing & Pin Connections**

**Drawing No:** M

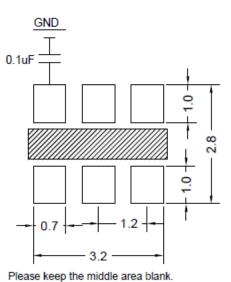
MD240085-1







PIN	Function				
#1	Control Voltage				
#2	OE				
#3	GND				
#4	OUTPUT				
#5	OUTPUT_N				
#6	Supply Voltage				



Do not layout any lines in this space. To ensure optimal oscillator performance, place a by-pass capacitor of 0.1µF as close to the part as possible between Vcc and GND pads

Unit in mm 1mm = 0.0394 inches



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## VCXO3225BM-LJ\_CML-112

Low Jitter VCXO\_Voltage Controlled Crystal Oscillator

## **Specifications**

Oscillator	Cum	Condition	Value			Unit	Note
Specification	Sym		Min.	Тур.	Max.		
Operational Frequency	f <sub>0</sub>		15		2100	MHz	
RF Output							
Output Waveform				CML			
Output Level		Output high	Vcc-0.085		Vcc	V	
		Output low	Vcc-0.6		Vcc-0.32	V	
Duty Cycle			45		55	%	
Rise & Fall Time					0.35	ns	
Startup Time					8	ms	
Tri-State		Enable	0.7 V <sub>cc</sub>			V	
(Input to Pin2)		Disable			0.3 V <sub>cc</sub>	V	
Power Supply							
Voltage	Vcc	±10%		2.5		V	
Supply Current		V <sub>cc</sub> =2.5V			80	mA	
Stand by Current		V <sub>cc</sub> =2.5V			80	mA	
Control Voltage							
Control Voltage		V <sub>cc</sub> =2.5V	0.25	1.25	2.25	V	
Pulling Range			±50		±250	ppm	
Linearity					±10	%	
Modulation Bandwidth			5		20	KHz	
VC Input Impedance			5			Mohm	
Frequency Stability							
Versus Temperature					±20	ppm	
Phase Noise At $V_{cc}$ =3.3 $V$ , 805.664MHz Frequency		1KHz		-107		dBc/Hz	
		10KHz		-117			
		100KHz		-125			
		1MHz		-135			
RMS Phase Jitter		Integrated 12KHz-20MHz	150		300	fs	
Period Jitter					50	ps	
<b>Environmental Conditio</b>							
Operating temperature ra	nge	-10°C to +60°C					