

Dynamic Engineers Inc.

2550 Gray Falls Dr., Suite#128, Houston, TX, 77077 USA TEL: 1-281-870-8822 EMAIL: Sales@DynamicEng.com

Features and Benefits

Frequency range: 122.88MHz

Output: LVPECL Supply voltage: 3.3V Current:110mA Max.

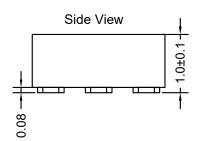
Frequency stability vs. temperature: ±100PPM Operating temperature: -10°C to +60°C

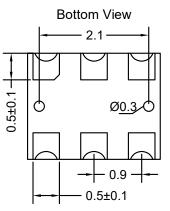
Size: 2.5x2x1mm Package type: SMD

Typical Applications

Defense Systems
Mobile Radar Station
Gigabit Ethernet, SONET/SDH
Server & Storage, Data Center
SD/HD Video, FPGA Clock Generation

Mechanical Drawing & Pin Connections

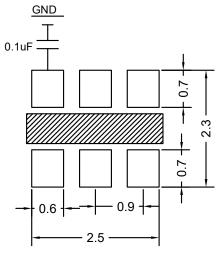




PIN	Function				
#1	Control Voltage				
#2	OE				
#3	GND				
#4	OUTPUT				
#5	OUTPUT_N				
#6	Supply Voltage				

Drawing No:

MD240070-1



Please keep the middle area blank.
Do not layout any lines in this space.
To ensure optimal oscillator performance, place a by-pass capacitor of 0.1µF as close to the part as possible between Vcc and GND pads

Unit in mm 1mm = 0.0394 inches



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CdW///Whicbg

Oscillator	Sym	Condition	Value			Unit	Note
Specification			Min.	Тур.	Max.		
Operational Frequency	f_0			122.88		MHz	
RF Output							
Output Waveform			LVPECL				
Output Level		Output high	Vcc-1.165		Vcc-0.8	V	
		Output low	Vcc-2.0		Vcc-1.55	V	
Duty Cycle			45		55	%	
Rise & Fall Time					0.35	ns	
Startup Time					8	ms	
Tri-State		Enable	0.7 V _{cc}			V	
(Input to Pin2)		Disable			0.3 V _{cc}	V	
Power Supply							
Voltage	Vcc	±10%		3.3		V	
Supply Current					110	mA	
Control Voltage							
Control Voltage	Vc		0.3	1.65	3	V	
Pulling Range			±50		±250	ppm	
Linearity					±10	%	
Frequency Stability							
Versus Temperature					±100	ppm	
RMS Phase Jitter		Integrated 12KHz- 20MHz			300	fs	
Environmental Condition							
Operating temperature ra	nge	-10°C to +60°C					