



Features and Benefits

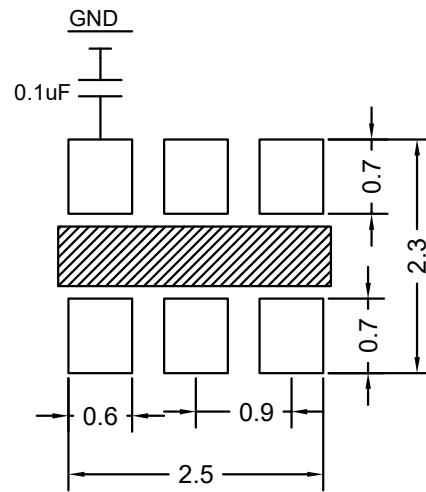
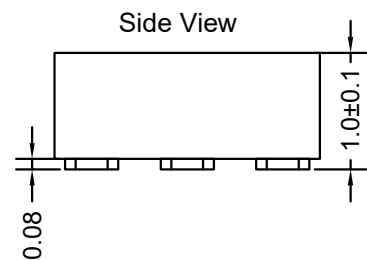
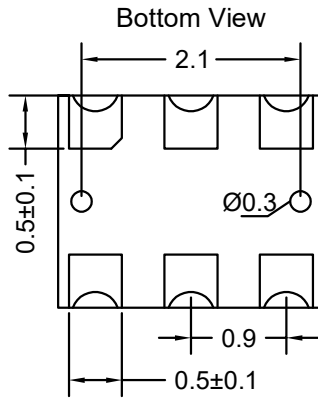
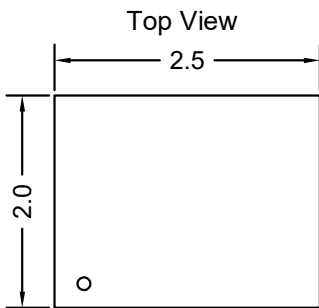
- Frequency range: 122.88MHz
- Output: LVDS
- Supply voltage: 3.3V
- Current:90mA Max.
- Frequency stability vs. temperature: ± 25 PPM
- Operating temperature: -20°C to +70°C
- Size: 2.5x2x1mm
- Package type: SMD

Typical Applications

- Defense Systems
- Mobile Radar Station
- Gigabit Ethernet, SONET/SDH
- Server & Storage, Data Center
- SD/HD Video, FPGA Clock Generation

Mechanical Drawing & Pin Connections

Drawing No: MD240070-1



PIN	Function
#1	Control Voltage
#2	OE
#3	GND
#4	OUTPUT
#5	OUTPUT_N
#6	Supply Voltage

Unit in mm
1mm = 0.0394 inches

Please keep the middle area blank.
Do not layout any lines in this space.
To ensure optimal oscillator performance, place a by-pass capacitor of 0.1µF as close to the part as possible between Vcc and GND pads



Oscillator Specification	Sym	Condition	Value			Unit	Note
			Min.	Typ.	Max.		
Operational Frequency	f ₀			122.88		MHz	
RF Output							
Output Waveform			LVDS				
Output Level		Output high			1.6	V	
		Output low	0.9			V	
Duty Cycle			45		55	%	
Rise & Fall Time					0.35	ns	
Startup Time					8	ms	
Tri-State (Input to Pin2)		Enable	0.7 V _{CC}			V	
		Disable			0.3 V _{CC}	V	
Power Supply							
Voltage	V _{CC}		2.97	3.3	3.63	V	
Supply Current					90	mA	
Control Voltage							
Control Voltage	V _C		0.3	1.65	3	V	
Pulling Range			±50		±250	ppm	
Linearity					±10	%	
Frequency Stability							
Versus Temperature					±25	ppm	
RMS Phase Jitter		Integrated 12KHz-20MHz			300	fs	
Environmental Conditions							
Operating temperature range		-20°C to +70°C					