

# Dynamic Engineers Inc.

Website: <u>www.DynamicEngineers.com</u> Email: <u>Inquiry@DynamicEngineers.com</u>

## **Features and Benefits**

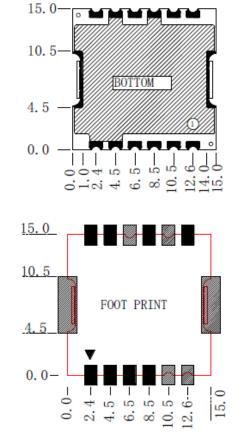
Frequency Range: 740-800MHz Supply Voltage: 5.0V Current: 35mA Max. 3 wire serial integer PLL Output power: +8.0dBm Max. Phase noise: -109dBc/Hz@10KHz Operating temperature: -40°C--+85°C Size: 15x15x3.7mm Package type: SMD

## **Typical Applications**

Base station Cable TV Satellite Communications UHF radio link

## **Mechanical Drawing & Pin Connections**





PLL1515BQ-IT-740MHz-800MHz-A 740-800MHz SMD Phase Locked Loop (PLL) Synthesizer

#### Drawing No: M

MD240065-2

#### Pin Connections:

Pin	Function			
1	CLK			
2	DATA			
3	LE			
4 <b>-</b> 6	GND			
7	VCC(VCO)			
8	GND			
9	RF Out			
10	GND			
11	Lock Detect			
12	Vcp(PLL)			
13&14	GND			

Unit in mm 1mm = 0.0394 inches

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Rev. 1

Dynamic Engineers reserves the right to make changes to the company datasheet(s) along with other information contained inside; such as data tables and araphs without notification to potential customers who may have earlier revisions in their possession.



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### PLL1515BQ-IT-740MHz-800MHz-A 740-800MHz SMD Phase Locked Loop (PLL) Synthesizer

## **Specifications**

Parameter		Units	Min.	Typical	Max.
Operating Frequency Range		MHz	740		800
Output Power		dBm	+4.0	+6.0	+8.0
Step Size		KHz		50	
Settling Time		msec		10	30
Output Impedance		Ω		50	
Phase Noise	@1kHz offset	dBc / Hz		-72	-77
Phase Noise	@10kHz offset	dBc / Hz		-109	-104
Phase Noise	@100kHz offset	dBc / Hz		-131	-126
Phase Noise	@1MHz offset	dBc / Hz		-150	-145
2nd Harmonic Suppression		dBc		-28	-20
Sideband Spurious Suppression		dBc			-75
Reference (frequency)		MHz		13	
Reference (Freq. Stability)		ppm			2.5
VCO Power Supply (Vcc) Voltage		Vdc		5	
VCO Power Supply (Vcc) Current		mA		25	35
PLL Power Supply (Vcp) Voltage		Vdc		5	
PLL Power Supply (Vcp) Current		mA		5	10
Operating Temperature Range		C°	-40		+85